

PTO/SB/08A (08-03)
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Substitute for form 1449A/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Application Number	10800938
(use as many sheets as necessary)				Filing Date	3/15/2004
				First Named Inventor	G. Glenn Henry
				Group Art Unit	2132
				Examiner Name	Gilberto Barron
Sheet	1	of	1	Attorney Docket Number	CNTR.2072

U.S. PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS

Examiner Signature	/Benjamin Lanier/	Date Considered	08/15/2007
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NON PATENT LITERATURE DOCUMENTS

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		Country Code ² - Number ⁴ - Kind Code ⁵ (if known)			
/BL/		CN1309351A (US Patent 6983374 provided herewith as an English-Language Equivalent)	08-22-2001	Hashimoto et al.	

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U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ² (known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
/BL/		6789147	09-07-2004	Kessler et al.	
/BL/		6026490	02-15-2000	Johns-Vano et al.	
/BL/		5134713	07-28-1992	Miller et al.	
/BL/		20010050989	12-13-2001	Jabari Zakiya	
/BL/		20040047466	03-11-2004	Feldman et al.	
/BL/		6246768	06-12-2001	Yong-Duk Kim	

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Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
/BL/		FISCHER et al. "Two Methods of Rijndael Implementation In Reconfigurable Hardware." Springer-Verlag Berlin Heidelberg 2001. CHES 2001. LNCS 2162. pp 77-92.	

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Substitute for form 1449A/PTO				Complete if Known	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Application Number	10800938
(use as many sheets as necessary)				Filing Date	3/15/2004
				First Named Inventor	G. Glenn Henry
				Group Art Unit	2132
				Examiner Name	Gilberto Barron
				Attorney Docket Number	CNTR.2072
Sheet	1	of	2		

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Examiner Signature	/Benjamin Lanier/	Date Considered	08/15/2007
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				First Named Inventor	G. Glenn Henry
				Group Art Unit	2132
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Sheet	2	of	2	Attorney Docket Number	CNTR.2072

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/BL/		LEE et al. "Efficient Permutation Instructions for Fast Software Cryptography." IEEE Micro, Nov 2001. Pages 56-69	

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		Country Code ³ - Number ⁴ - Kind Code ⁵ (If Known)			
/BL/	AA	WO0184769A	11-08-2001	Lindemann et al.	
/BL/	AB	CN1431584A (US20030172252A1 is an English-Language Equivalent Application)	07-23-2003	Henry et al.	

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/BL/	BA	TONG et al. "A system level Implementation of Rijndael on a memory-slot based FPGA card." <i>Field-Programmable Technology</i> , 2002. (FPT). 16 December 2002 (2002-12-16), pages 102-109, XP010636515 ISBN: 0-7803-7574-2	
/BL/	BB	TAYLOR D E et al. "Dynamic Hardware Plugins: Exploiting Reconfigurable Hardware for High-Performance Programmable Routers." <i>Computer Networks</i> , Elsevier Science Publishers B.V., Amsterdam, NL, vol. 38, no. 3. 21 February 2002 pages	
		295-310, XP004333786, ISSN: 1389-1286	
/BL/	BC	ELBIRT A J et al. "Instruction-Level Distributed Processing for Symmetric-Key Cryptography." <i>Parallel and Distributed Processing Symposium</i> . 2003. 22 April 2003, pages 78-87. XP010645610 ISBN: 0-7695-1926-1	

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Substitute for form 1449A/PTO				<i>Complete if Known</i>	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application Number	10/800938
				Filing Date	03/15/04
				First Named Inventor	G. Glenn Henry
				Group Art Unit	2131
				Examiner Name	
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GSA FPMR (41 CFR) 101-11, PAYMENT METHODS, 02/01/2002

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/BL/		ULMANN, B. "Designing a NICE Processor." Microprocessors and Microsystems, IIPC Business Press LTD. London, GB.	
		Vol. 23, No. 5. 25 October 1999. Pages 257-264. XP004321479 ISSN: 0141-9331.	

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Examiner Initials ^a	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T ⁶
		Country Code ³ - Number ⁴ - Kind Code ^{5 (if known)}				
/BL/	AA	WO0076119A1	12-14-2000	Daniel Z. Simon		
/BL/	AB	EP0913767A2	05-06-1999	Moyer et al.		
/BL/	A	JP57176457 (abstract)	10-29-1982	Kato Takeshi		
/BL/	AD	WO0144900A2	6-21-2001	Fleming et al.		

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Substitute for form 1449A/PTO				<i>Complete if Known</i>	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application Number	10/800938
				Filing Date	03/15/04
				First Named Inventor	G. Glenn Henry
				Group Art Unit	2131
				Examiner Name	
Sheet	2	of	2	Attorney Docket Number	CNTR.2072

NON-PATENT LITERATURE DOCUMENTS

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				Filing Date	03/15/04
				First Named Inventor	G. GLENN HENRY
				Group Art Unit	2131
				Examiner Name	UNKNOWN
Sheet	2	of	2	Attorney Docket Number	CNTR.2072

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
/BL/	BA	"IBM PCI Cryptographic Coprocessor CCA Basic Services Reference and Guide for IBM 4758 Models 002 and 023 with Release 2.40," IBM, September 2001, XP002291430	

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)				Application Number	NA
				Filing Date	Herewith
				First Named Inventor	G. Glenn Henry Thomas A. Crispin Terry Parks
				Group Art Unit	
				Examiner Name	
Sheet	1	of	2	Attorney Docket Number	CNTR.2072

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OTHER PRIOR ART-NON PATENT LITERATURE DOCUMENTS

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